

## FORM PTO-1449

INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT(S)

Atty Docket No. : 29273/566  
 Serial No. : (Con. of 09/886,267)  
 Inventors : T. NISHIMUKAI et al.  
 Filed :  
 Group Art Unit : 2183 (Anticipated)  
 Examiner : D. P. Pan (Anticipated)

## U.S. PATENT DOCUMENTS

Examiner Initial	Patent Number	Patent Date	Name	Class/Subclass	Filing Date
HBP	4,989,140	01/91	NISHIMUKAI et al.	364/200	
	4,775,955	10/88	LIU	395/425	10/85
	4,740,889	04/88	MOTERSOLE et al.	364/200	06/26/84
	4,719,568	01/88	CARRUBBA et al.	364/200	
	4,713,755	12/87	WORLEY, Jr. et al.	395/375	06/85
	4,713,752	12/87	TONE	364/200	12/84
	4,707,784	11/87	RYAN et al.	364/200	
	4,701,844	10/87	THOMPSON et al.	364/200	
	4,675,806	06/87	UCHIDA	364/200	03/03/82
	4,646,233	02/87	WEATHERFORD	364/200	
	4,608,633	08/86	BOOTHROYD et al.	364/200	04/01/83
	4,602,368	07/86	CIRCELLO et al	364/900	04/15/83
	4,551,798	11/85	HORVATH	364/200	
	4,551,799	11/85	RYAN et al.	364/200	
	4,530,050	07/85	FUKUNAGA et al.	364/200	08/82
	4,521,851	06/85	TRUBISKY et al.	364/200	10/13/82
	4,521,850	06/85	WILHITE et al.	364/200	
	4,502,110	02/85	SAITO	364/200	
	4,481,573	11/84	FUKUNAGA et al.	364/200	
	4,471,432	09/84	WILHITE et al.	364/200	

EXAMINER

/Hetul Patel/

DATE CONSIDERED

09/28/2006

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

## FORM PTO-1449

INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT(S)

Atty Docket No. : 29273/566  
 Serial No. : (Con. of 09/886,267)  
 Inventors : T. NISHIMUKAI et al.  
 Filed :  
 Group Art Unit : 2183 (Anticipated)  
 Examiner : D. P. Pan (Anticipated)

## U.S. PATENT DOCUMENTS

Examiner Initial	Patent Number	Patent Date	Name	Class/ Subclass	Filing Date
HBP	4,471,429	09/84	PORTER et al.	364/200	
	4,442,488	04/84	HALL	364/200	
	4,439,829	03/84	TSIANG	364/200	
	4,456,954	06/84	BULLIONS, III et al.	395/400	
	4,342,078	07/82	TREDENNICK et al.	364/200	
	4,332,010	05/82	MESSINA ET AL.	711/3	
	4,325,121	04/82	GUNTER et al.	364/200	
	4,322,795	03/82	LANGE et al.	395/425	
	4,312,034	01/82	GUNTER et al.	364/200	
	4,295,193	10/81	POMERENE	395/375	
	4,274,953	04/81	DOUGLAS et al.	209/326	
	4,179,736	12/79	WILHITE et al.	364/200	
	4,011,547	03/77	KIMMEL	364/200	
	3,979,726	09/76	LANGE et al.	364/200	
	3,859,636	01/75	COOK	364/200	
	3,845,474	10/74	LANGE et al.	364/200	
	3,781,823	12/73	SENESE	364/200	
	3,618,041	11/71	HORIKOSHI et al.	364/200	
	4,439,829	03/84	TSIANG	711/118	
	4,441,155	04/84	FLETCHER et al.	711/3	

EXAMINER

DATE CONSIDERED

/Hetul Patel/09/28/2006

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

## FORM PTO-1449

INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT(S)

Atty Docket No. : 29273/566  
 Serial No. : (Con. of 09/886267)  
 Inventors : T. NISHIMUKAI et al.  
 Filed :  
 Group Art Unit : 2183 (Anticipated)  
 Examiner : D. P. Pan (Anticipated)

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class/ Subclass	Translation Yes No
	50-138738	11/05/75	Japan		ABS.
	57-15494	08/30/80	Japan		X

## OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner Initial

1. ~~Integrated MMU Cache/Raise System Level Issues, Serin Lazebnik et al., Computer Design, Vol. 26, No. 10, May 15, 1987, pp. 75-79.~~
2. ~~MC68020 32-BIT MICROPROCESSOR USER'S MANUAL, Second Edition, Englewood Cliffs, NJ, PRENTICE HALL, INC., 1984, PP. 1-2, 1-9/1-10, 6-8, 6-9, 7-1 TO 7-4.~~
3. ~~Computing Survey, Vol. 14, No. 3, Sep. 3, 1982, pp. 473-530, A.J. Smith: Cache Memories.~~
4. ~~Electronics International, Vol. 55, No. 16, Aug. 1982, pp. 112-117, Per Knudsen: Supermini Goes Microprocessor Route to Put it Up Front in Performance.~~

EXAMINER

/Hetul Patel/

DATE CONSIDERED

09/28/2006

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

**FORM PTO-1449**

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT(S)**

Atty Docket No. : 29273/566  
Serial No. : (Con. of 09/886,267)  
Inventors : T. NISHIMUKAI et al.  
Filed :  
Group Art Unit : 2183 (Anticipated)  
Examiner : D. P. Pan (Anticipated)

**OTHER DOCUMENTS**  
(Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner  
Initial \_\_\_\_\_

5. ~~Computer Design, Vol. 24, No. 3, Mar. 1985, pp. 173-181, T. Fessum et al.: New VAX Squeezes Mainframe Power Into Mini Package.~~
6. ~~"COMPTON '84", 28th IEEE Computer Society International Conference, digest of papers, *Intellectual Leverage The Driving Technologies*, Feb. 27, 1984, pp. 230-236, David Stevenson, Z80,000 32-Bit Microprocessor.~~
7. ~~Microcomputer Hardware, *The Iwanami Microelectronics Course*, Nov. 1984, Japan, pp. 144-145.~~
8. ~~A Cache Memory for the NS16032 Virtual Memory Memory Processor, R.S. Lim, 1984, IEEE, pp. 377-381.~~

EXAMINER

/Hetul Patel/

DATE CONSIDERED

09/28/2006

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.